

501.43323X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): A. NISHIDA, et al
Serial No.: (not yet assigned)
Filed: February 20, 2004
For: A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND A
METHOD OF MANUFACTURING THE SAME
Group:

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 20, 2004

Sir:

The following preliminary amendments and remarks are respectfully
submitted in connection with the above-identified application, as listed below and as
set forth on the following pages:

Substitute Specification and Marked Up Version of Original Specification;
Amendment of the Abstract; and
Remarks are included following the amendments.